

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Michitaka KAMEYAMA et al.

Serial No. 10/502,265

Confirmation No. 9388

Filed: July 22, 2004

For: Logical Operation Circuit and Logical
Operation Method

Art Unit: 2819

Examiner: Tan, Vibol

**RESPONSE TO NOTICE OF NON-COMPLIANT
AMENDMENT**

Mail Stop Petition
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Non-Compliant Amendment mailed on December 18, 2006, applicant replies as follows:

Remarks/Arguments begin on page 2 of this paper.

A Declaration under 37 CFR 1.132 and a copy of a previously-filed **Terminal Disclaimer** are attached following page 2.